

REISSUE PATENT APPLICATION TRANSMITTAL

ADDRESS TO:

Assistant Commissioner for Patents
Box Reissue
Washington, D.C. 20231

Attorney Docket No.

2037.1002-003

First Named Inventor

Richard C. Foss

Original Patent Number

5,414,662

Original Patent Issue Date
(Month/Day/Year)

May 9, 1995

Express Mail Label No.

EV 010604173 US

APPLICATION FOR REISSUE OF:

☒ Utility Patent☐ Design Patent☐ Plant Patent

(Check applicable box)

Title of
Invention

DYNAMIC RANDOM ACCESS MEMORY USING IMPERFECT ISOLATING TRANSISTORS

APPLICATION ELEMENTS (37 C.F.R. 1.173)

1. ☐ Reissue Application Fee Transmittal Form
(Submit an original, and a duplicate for fee processing)
2. ☒ Specification and Claims in double column copy of patent
format (amended, if appropriate)
3. ☒ Drawing(s) (proposed amendments, if appropriate)
4. ☐ Reissue Oath/Declaration (original or copy)
5. ☒ Original U.S. Patent currently assigned?
☒ Yes ☐ No
(If Yes, check applicable box(es))
6. ☐ Written Consent of all Assignees
7. ☐ 37 C.F.R. §3.73(b) Statement
8. ☐ CD-ROM or CD-R in duplicate, large table or Computer
Program (Appendix)
9. ☐ Nucleotide and/or Amino Acid Sequence Submission
(if applicable, all of the following are necessary)
 - a. ☐ Computer Readable Form
 - b. ☐ Paper Copy (identical to computer copy)
☐ Pages
 - c. ☐ Statements verifying identity of above copies

ACCOMPANYING APPLICATION PARTS

10. ☒ Statement of status and support for all changes to the
claims. See 37 C.F.R. 1.173(c).
11. ☐ Original U.S. Patent for surrender
☐ Ribboned Original Patent Grant
☐ Statement of Loss
12. ☐ Foreign Priority Claim (35 U.S.C. 119 or 365) (if applicable)
13. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS
Citations
14. ☐ English Translation of Reissue Oath/Declaration
(if applicable)
15. ☐ Power of Attorney
16. ☐ Small Entity Statement(s) ☐ Statement filed in prior application,
status still proper and desired
17. ☐ Preliminary Amendment
18. ☒ Return Receipt Postcard (MPEP 503)
19. ☐ Other: _____

19 CORRESPONDENCE ADDRESS

NAME	Customer No. 021005				
	HAMILTON, BROOK, SMITH & REYNOLDS, P.C.				
ADDRESS	530 Virginia Road, P.O. Box 9133				
CITY	Concord	STATE	MA	ZIP CODE	01742-9133
COUNTRY	USA	TELEPHONE	(978) 341-0036	FAX	(978) 341-0136

Signature	<i>Gerald M. Bluhm</i>	Date	12/21/01
Submitted by Typed or Printed Name	Gerald M. Bluhm	Reg. Number	44,035

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Richard C. Foss, Peter B. Gillingham, Robert
Harland, Masami Mitsuhashi and Atsushi Wada

Title: Dynamic Random Access Memory Using Imperfect
Isolating Transistors

Reissue Application of U.S. Patent No. 5,414,662

Date: 12/21/01 Express Mail No. EV 010604173 US

STATEMENT OF STATUS AND SUPPORT FOR ALL CLAIMS

Assistant Commissioner for Patents

Box Reissue

Washington, D.C. 20231

Sirs:

The following list identifies support for new Claims 18-25 as required by 37 C.F.R. § 1.173(c). In many cases, additional support for a particular claim can be found elsewhere in the specification.

Claims 1-17 are originally issued Claims of United States Patent No. 5,414,662 and are cancelled in this reissue continuation application. However, they have been allowed, some in amended form, in the parent case to this application.

Claim	Status	Support
18	New Pending	See Column 5, lines 32-68
19	New Pending	See Column 5, lines 21-26
20	New Pending	See Column 5, lines 43-45

Claim	Status	Support
21	New Pending	See Column 6, lines 22-31
22	New Pending	See Column 5, lines 32-68
23	New Pending	See Column 5, lines 21-26
24	New Pending	See Column 5, lines 43-45
25	New Pending	See Column 6, lines 22-31

Respectfully submitted,

HAMILTON, BROOK, SMITH & REYNOLDS, P.C.

By *Gerald M. Bluhm*
Gerald M. Bluhm
Registration No. 44,035
Telephone (978) 341-0036
Facsimile (978) 341-0136

Concord, Massachusetts 01742-9133

Dated: 12/21/01

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